

PATENT
450117-4278.3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: PAUL HODGINS et al.
Filed: Filed Concurrently Herewith
Title of Invention: METHOD AND APPARATUS FOR SERVING
DATA



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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
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Washington, D.C. 20231

Sir:

Applicants bring to the attention of the Examiner the
prior art cited in the parent application, serial no. 08/979,474.
Also enclosed is a copy of Form PTO-1449.

REMARKS

Entry of this Information Disclosure Statement and an
early examination on the merits are respectfully solicited.


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Respectfully submitted,

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	450117-4278.3	N/A
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J:836 U.S. PTO
 09/652850
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	US 5 095 480 A	3/10/92	Peter R. Fenner			
	AB	US 5 323 389 A	6/21/94	Francois J. Bitz et al.			
	AC	US 5 341 474 A	8/23/94	Alexander Gelman et al.			
	AD	US 5 371 532 A	12/6/94	Alexander Gelman et al.			
	AE	US 5 371 547 A	12/6/94	Robert J. Siracusa et al.			
	AF	US 5 481 687 A	1/2/96	Jozef A. O. Goubert et al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AG	EP 0 574 140 A	12/15/93	Europe				
	AH	EP 0 596 624 A	5/11/94	Europe				
	AI	EP 0 601 699 A	6/15/94	Europe				
	AJ	EP 0 605 115 A	7/6/94	Europe				
	AK	EP 0 633 694 A	1/11/95	Europe				
	AL	EP 0 651 391 A	5/3/95	Europe				
	AM	EP 0 667 713 A	8/16/95	EPO				
	AN	EP 0 680 236 A	11/2/95	Europe				
	AO	EP 0 696 798 A	2/14/96	Europe				
	AP	EP 0 735 758 A	10/2/96	Europe				
	AQ	FR 2 653 284 A	4/19/91	France				
	AR	GB 2 217 488 A	10/25/89	Great Britain				
	AS	WO 93 09623 A	5/13/93	PCT				
	AT	WO 96 08896 A	3/21/96	PCT				
	AU	WO 97 04596 A	2/6/97	PCT				

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		APPLICANT Paul Hodgins et al.		
		FILING DATE Filed Concurrently Herewith	GROUP	
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)				
	AV		Computer Networks and ISDN Systems, vol. 26, no. 10, 7/1/94, pages 1305-1322, XP000453512, S. Ramanathan et al.: "TOWARDS PERSONALIZED MULTIMEDIA DIAL-UP SERVICES".	
	AW		Computer Society International Conference (COMPCON), Spring Meeting, Los Alamitos, 2/26/90 - 3/2/90, No. Conf. 35, 2/26/90, Institute of Electrical and Electronics Engineers, pages 44-53, XP000146164, J. E. Murray et al.: "Micro-Architecture of The Vax 9000".	
	AX		Computer Technology Review, 12/21/94, pages 66, 68, 81-83, XP000429677, Tobagi F. et al.: "Streaming Raid-A Disk Array Management System For Video Files".	
	AY		IBM Technical Disclosure Bulletin, vol. 39, no. 4, 4/1/96, pages 161-163, XP000587459, "Weighted Queueing Algorithm For Efficient Asynchronous Transfer Mode Traffic Shaping".	
	AZ		IEICE TRANSACTIONS ON ELECTRONICS, vol. E78-C, no. 12, 12/1/95, pages 1738-1745, XP000555581, Yasuharu Tomimitsu et al.: "An ATM Chip Set For High Performance Computer Interfaces, Affording Over 100 MBPS Sustained Throughput".	
	AAA		Interfaces In Computing, vol. 3, no. 3/4, 12/85, Lausanne CH, pages 173-187, XP002005672, R.W. Robinson et al.: "Interfacing to Ethernet Using VLSI protocol chips", page 179, lines 11-39.	
	AAB		Multimedia Computing and Networking, 1/96, USA, pages 410-421, XP000675452, M. Kumar et al.: "A High Performance Video Server For Broadband Network Environment".	
	AAC		Serving Humanity Through Communications, Supercomm/ICC, New Orleans, 5/1/94-5/5/94, vol. 2, 5/1/94, Institute of Electrical and Electronics Engineers, Yao-Tzung Wang et al.: "An Improved Scheduling Algorithm for Weighted Round-Robin Cell Multiplexing in an ATM Switch", page 1034, column 2, lines 35-44.	

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